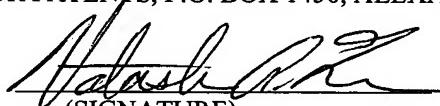


09/04/03  
14230 U.S. PTO

PATENT APPLICATION  
Docket No. 9898-293  
Client No. SS-17762-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

EXPRESS MAIL	MAILING LABEL NO. <u>EL982131044US</u>
	DATE OF DEPOSIT: <u>SEPTEMBER 4, 2003</u>
<p>I HEREBY CERTIFY THAT THIS PAPER AND ENCLOSURES AND/OR FEE ARE BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO: MAIL STOP PATENT APPLICATION, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450.</p>	
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Mail Stop Patent Application  
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P.O. Box 1450  
Alexandria, VA 22313-1450

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor: Hoe-Ju CHUNG and Kyu-Hyoun KIM

For: SEMICONDUCTOR MEMORY DEVICE HAVING DUTY CYCLE CORRECTION CIRCUIT AND INTERPOLATION CIRCUIT  
INTERPOLATING CLOCK SIGNAL IN THE SEMICONDUCTOR MEMORY DEVICE

Applicant requests FIG. 5 to be published with the application.

Enclosures:

- Specification (pages 1-11); claims (pages 12-16); abstract (page 17)  
 SIX sheet(s) of FORMAL drawings (comprising 10 figures)  
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<u>CLAIMS AS FILED</u>				
For	Number Filed	Number Extra	Rate	Basic Fee
Total Claims	15-20	0	x \$ 18 =	\$0.00
Independent Claims	4-3	1	x \$ 84 =	\$84.00
TOTAL FILING FEE				\$834.00

17858 U.S. PTO  
10/656303  
09/04/03

- Certified Korean Priority Document No. 2002-53327, filed September 4, 2002
- Combined Declaration and Power of Attorney
- Newly executed (original or copy)
- Assignment with cover sheet
  - Assignee Name and Address: Samsung Electronics Co., Ltd.  
Suwon-city, Kyungki-do Republic of Korea
- PTO Form 2038 authorizing credit card payment of \$834.00, filing fee (\$750.00) and additional claims fee (\$84.00) is enclosed.
- Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

**Customer No. 20575**

Respectfully submitted,

MARGER JOHNSON & McCOLLUM, P.C.



\_\_\_\_\_  
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